

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroyuki OZAWA et al.**

Patent No.: **6,815,811**

Issued: **November 9, 2004**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT WITH DUMMY PATTERNS**

Attorney Docket Number: **011589**

Customer Number: **38834**

REQUEST FOR CERTIFICATE OF CORRECTION

Mail Stop: COC

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Date: April 4, 2005

Sir:

The undersigned requests that a Certificate of Correction be issued for the above-identified patent as indicated on the attached Form PTO-1050.

This request is being made in order to correct **the Specification**. Please correct the claims as amended in the Supplemental Notice of Allowability mailed on July 16, 2004. It is respectfully submitted that no new matter has been added.

Since this error is a Patent and Trademark Office printing error, it is respectfully submitted that no fee is required.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

Thomas E. Brown

Attorney for Applicants

Registration No. 44,450

Telephone: (202) 822-1100

Facsimile: (202) 822-1111

TEB/my

Enclosures: Form PTO-1050; Supplemental Notice of Allowability